Office. As the three-month shortened statutory period for reply is due June 9, 2005, Applicant

respectfully requests that this Response be considered timely filed.

AMENDMENTS

In the Specification

At page 16, lines 5-16, please amend the following paragraph:

Referring to Figure 2, there is shown a receiver (11) for receiving a transmission value

from the transmitter (10). The output of the receiver (11) is fed into a shift register (12). The

value in the shift register (12) is decoded by decoding logic (13) using an identify number

obtained from an ID register (14). The result obtained from the decoding logic (13) comprises a

decoded unit number and a decoded counter value and is stored in a decoded result register (15).

All the aforementioned steps are carried out under the control of a control unit (16). The decoder

microchip also comprises four counter registers respectively numbered (17), (18), (19), and (20)

in which decoder counter values may be stored and from where they may be compared with

decoded counter values obtained by the control unit (16) from the decoded result register (15).

Four outputs respectively numbered (21), (22), (23) and (24) are also provided and may be used

by the control unit (16) to indicate what kind of information has been received, *e.g.*, **indicating**

an activation of the transmitter (10) to a user .

In the Claims

Applicant respectfully submits that no amendments have been made to the

pending claims for the purpose of overcoming any prior art rejections that may restrict the literal

scope of the claims or equivalents thereof.